



2123^{#3}_{BT}
9-5-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Gary L. Swoboda
Serial No: 09/943,137
Filed: August 30, 2001
For: Using Selective Omission to Compress On-Chip Data Processor Trace and Timing
Information for Export

TI-30483

Art Unit: 2123

Examiner:

Conf. No.: 9656

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INFORMATION DISCLOSURE STATEMENT

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Robin E. Barnum

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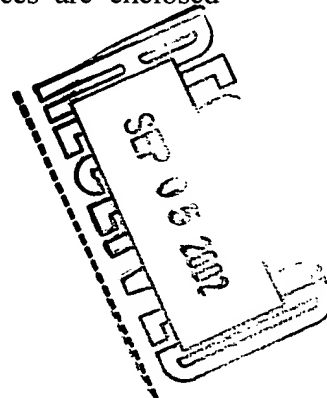
Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the noted references are enclosed herewith.

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Respectfully submitted,

Robert D. Marshall, Jr.
Attorney for Applicant(s)
Reg. No. 28,527

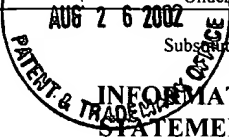
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				Application Number	
				Filing Date	August 30, 2001
				First Named Inventor	Gary L. Swoboda
				Group Art Unit	2123
				Examiner Name	
Sheet	1	of	1	Attorney Docket No.	TI-30483

U.S. PATENT DOCUMENTS

Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	ARM Limited, RDI 1.5.1tx and RDI 1.5.1rt; Doc. No. RDI-0032-CUST-ESPC-A; 19 May, 2000; pgs. 1-55.	
	CB	ARM Limited, ETM9, Rev. 1, Technical Reference Manual, Doc. No. DDI 0157C, pgs. i - Index-3.	
	CC	ARM Limited, Embedded Trace Macrocell, Rev. 1, Specification, Doc. No. IHI 0014E, pgs. i - Index-3.	
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